

STPS1045HR

Aerospace 2 x 10 A - 45 V Schottky rectifier

Features

■ Forward current: 2 x 10 A

■ Repetitive peak voltage: 45 V

■ Low forward voltage drop: 0.75 V

■ Maximum junction temperature: 175 °C

Negligible switching losses

■ Low capacitance

■ High reverse avalanche surge capability

■ Hermetic package

■ Target radiation qualification:

- 150 krad (Si) low dose rate

- 1 Mrad high dose rate

■ ESCC qualified



Description

This power Schottky rectifier is designed and packaged to comply with the ESCC5000 specification for aerospace products. Housed in a hermetically sealed surface mount package, it is ideal for use in applications for aerospace and other harsh environments.

The STPS1045HR is intended for use in medium voltage applications and in high frequency circuits where low switching losses and low noise are required.

Table 1. Device summary

Order code	ESCC detailed specification	Quality level	Configuration	Package	Lead finish	EPPL
STPS1045CS1	-	Engineering model	Double die common cathode	SMD.5	Gold	-
STPS1045CSHRB	5106/017/02	ESCC flight	common camode			-

Characteristics STPS1045HR

1 Characteristics

Table 2. Absolute maximum ratings

Symbol	Characteristic	Value	Unit
I _{FSM}	Forward surge current (per diode) ⁽¹⁾	200	Α
V _{RRM}	Repetitive peak reverse voltage ⁽²⁾	45	V
I _{RRM}	Repetitive peak reverse current ⁽³⁾	1	Α
	Average output rectified current (50% duty cycle):(4)		
Io	per diode	10	Α
	per device	20	
I _{F(RMS)}	Forward rms current (per diode)	15	Α
T _{OP}	Operating temperature range (case temperature)	-65 to +175	°C
T_J	Junction temperature	+175	°C
T _{STG}	Storage temperature range	-65 to +175	°C
T _{SOL}	Soldering temperature ⁽⁵⁾	+245	°C
dV/dt	Critical rate of rise of reverse voltage	10000	V/µs

- 1. Sinusoidal pulse of 10 ms duration
- 2. Pulsed, duration 5 ms, F = 50 Hz
- 3. Pulsed, duration 2 μ s, F = 1 kHz
- 4. For $T_{case} > +140$ °C, derate linearly to 0 A at +175 °C.
- 5. Duration 5 seconds maximum and the same package shall not be resoldered until 3 minutes have elapsed.

Table 3. Thermal resistance

Symbol	Characteristic	Value	Unit
R _{th(j-c)} ⁽¹⁾	Thermal resistance, junction to case per diode per device ⁽²⁾	1.65 0.85	°C/W

- 1. Package mounted on infinite heatsink
- 2. The per device ratings apply only when both anode terminals are tied togther.

STPS1045HR Characteristics

Table 4. Electrical measurements at ambiant temperature (per diode), $T_{amb} = 22 \pm 3$ °C

Symbol Characteristic		MIL-STD-750	Test conditions	Values		Units
Symbol	tes	test method	rest conditions	Min.	Max.	Units
I _R	Reverse Current	4016	DC method, V _R = 45V	-	100	μΑ
V _{F1} ⁽¹⁾			Pulse method, I _F = 3 A	-	620	mV
V _{F2} ⁽¹⁾	Forward Voltage	4011	Pulse method, I _F = 20 A	-	750	mV
V _{F3} ⁽¹⁾			Pulse method, I _F = 20 A		880	mV
С	Capacitance	4001	V _R = 5 V, F = 1 MHz	-	500	pF
Z _{th(j-c)} ⁽²⁾	Relative thermal impedance, junction to case	3101	$I_H = 15 \text{ to } 40 \text{ A}, t_H = 50 \text{ ms}$ $I_M = 50 \text{ mA}, t_{md} = 100 \mu\text{s}$	Calculat	e ΔV _F ⁽³⁾	°C/W

^{1.} Pulse width \leq 300 μ s, Duty Cycle \leq 2%

Table 5. Electrical measurements at high and low temperatures (per diode)

Symbol Characteristic		MIL-STD-750	Test conditions ⁽¹⁾	Values		Units	
Syllibol	Cilaracteristic	test method	rest conditions.	Min.	Max.	Ointo	
I _R	Reverse Current	4016	T_{case} = +125 (+0, -5) °C DC method, V_R = 45 V	-	15	mA	
V _{F1} ⁽²⁾			T_{case} = +125 (+0, -5) °C pulse method, I_F = 3 A	-	570	mV	
V _{E2} ⁽²⁾	Forward Voltage	4011 -	4011	$T_{case} = +125 (+0, -5)$ °C pulse method, $I_F = 10$ A	-	700	mV
V F2` ′	Forward voltage		T_{case} = -55 (+0, -5) °C pulse method, I_F = 10 A	-	850	mV	
V _{F3} ⁽²⁾			$T_{case} = +125 (+0, -5) ^{\circ}C$ pulse method, $I_F = 20 A$	-	800	mV	

^{1.} Read and record measurements shall be performed on a sample of 5 components with 0 failures allowed. Alternatively a 100% inspection may be performed.

^{2.} Performed only during screening tests parameter drift values (initial measurements), go-no-go

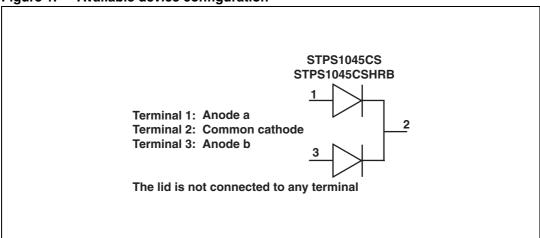
^{3.} The limits for ΔVF shall be defined by the manufacturer on every lot in accordance with MIL-STD-750 Method 3101 and shall guarantee the $R_{th(j-c)}$ limits specified in maximum ratings.

^{2.} Performed only during screening tests parameter drift values (initial measurements for HTRB), go-no-go.

Configuration STPS1045HR

2 Configuration

Figure 1. Available device configuration



Package Information 3

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

D b

Figure 2. Surface mount package (SMD.5), 3-terminal dimension definitions

Table 6. Surface mount package (SMD.5), 3-terminal dimension values

Reference	Dimension in	n millimetres	Dimlension in inches		
neierence	Min.	Max.	Min.	Max.	
А	2.84	3.15	0.112	0.124	
A1	0.25	0.51	0.010	0.20	
b	7.13	7.39	0.281	0.291	
b1	5.58	5.84	0.220	0.230	
b2 ⁽¹⁾	2.28	2.54	0.090	0.100	
b3 ⁽¹⁾	2.92	3.18	0.115	0.125	
D	10.03	10.28	0.395	0.405	
D1 ⁽¹⁾	0.76	-	0.030	-	
E	7.39	7.64	0.291	0.301	
e ⁽¹⁾	1.91	BSC	0.0)75	

1. 2 locations

4 Ordering Information

Table 7. Ordering information

Order code	ESCC detailed specification	Package	Lead finish	Marking	EPPL	Mass (g)	Packing
STPS1045CS1	-	SMD.5	Gold	STPS1045CS1	-	2.0	Strip
STPS1045CSHRB	5106/017/02	SIVID.3	Gold	510601702	-	2.0	pack

STPS1045HR Revision history

5 Revision history

Table 8. Document revision history

Date	Revision	Changes
16-June-2010	1	Initial release.

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